

# PT32L/F005 Series

## SELECTION GUIDE



Deep sleep mode current < 1μA

Part Number	Memory		Core	CPU Frequency	Operating Voltage	GPIO		Timer & Counter				Communication Interface			Clock			Analog		Security	Operating Temperature	SWD Pins	Package Information			
	Flash (K)	RAM (K)		MHz	VDD (V)	I/O	Adv	Base	Low Power	WDG	UART	I2C	SPI	HSI (MHz)	LSI (KHz)	PLL (mult)	ADC 12-bit	PVD	CRC	°C	Package Types		Thickness	Pin Pitch	Packing	
PT32L005F6P6	32	2	M0	48	1.8-5.5	17+1	1	2	1	1	2	1	1	24	32	2	8ext+3int	✓	✓	-40~85	17, 18	TSSOP20	1.2mm	0.65mm	Tube	
PT32L005F6X6	32	2	M0	48	1.8-5.5	17+1	1	2	1	1	2	1	1	24	32	2	8ext+3int	✓	✓	-40~85	14, 15	QFN20(3*3)	0.75mm	0.4mm	Tray	
PT32L005F6X6*	32	2	M0	48	1.8-5.5	17+1	1	2	1	1	2	1	1	24	32	2	8ext+3int	✓	✓	-40~85	14, 15	QFN20(3*3)	0.75mm	0.4mm	Tape & Reel	
PT32F005F6P6	32	2	M0	48	2.2-5.5	17+1	1	2	-	1	2	1	1	24	32	2	8ext+3int	✓	✓	-40~85	8, 18	TSSOP20	1.2mm	0.65mm	Tube	
PT32F005F6X6*	32	2	M0	48	2.2-5.5	17+1	1	2	-	1	2	1	1	24	32	2	8ext+3int	✓	✓	-40~85	5, 15	QFN20(3*3)	0.75mm	0.4mm	Tray	
PT32F005F6X6*	32	2	M0	48	2.2-5.5	17+1	1	2	-	1	2	1	1	24	32	2	8ext+3int	✓	✓	-40~85	5, 15	QFN20(3*3)	0.75mm	0.4mm	Tape & Reel	

\* Under Development